

PATENT NUMBER _____

U.S. **UTILITY** Patent Application

SCANNED

O.I.P.E.

Q.A.

PATENT DATE

APPLICATION NO.

09/651422

CONT/PRIOR

CLASS

SUBCLASS

ART UNIT

EXAMINER

APPLICANTS

Jeffrey Honeycutt
Lamar, South Carolina
Daniel Smith

THE

Transistor structure, in three
"methods of" forming transistor

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PTO-2040
12/99

ISSUING CLASSIFICATION

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
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<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)		ISSUE FEE Amount Due Date Paid	
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